

FIG.1

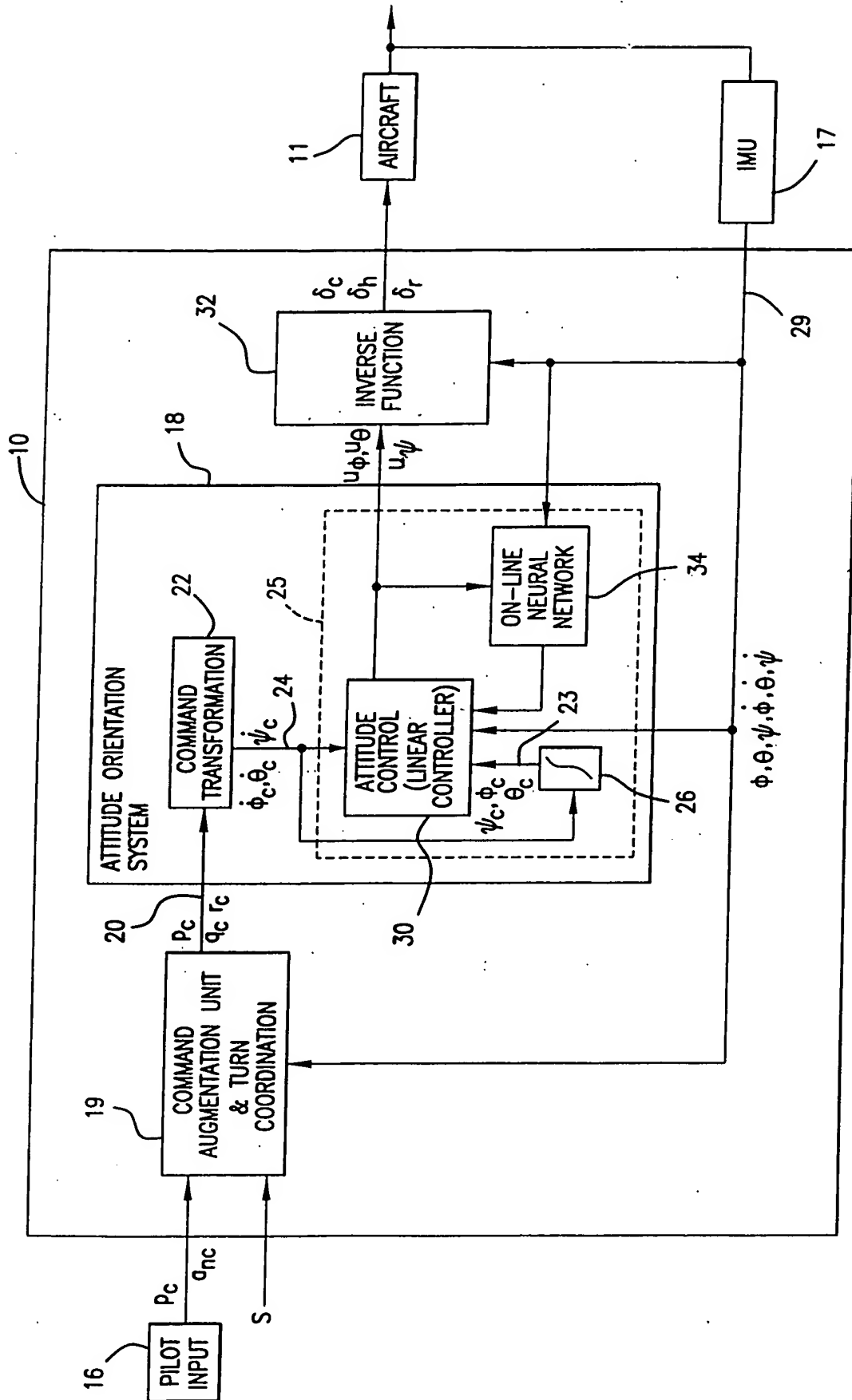


FIG.2

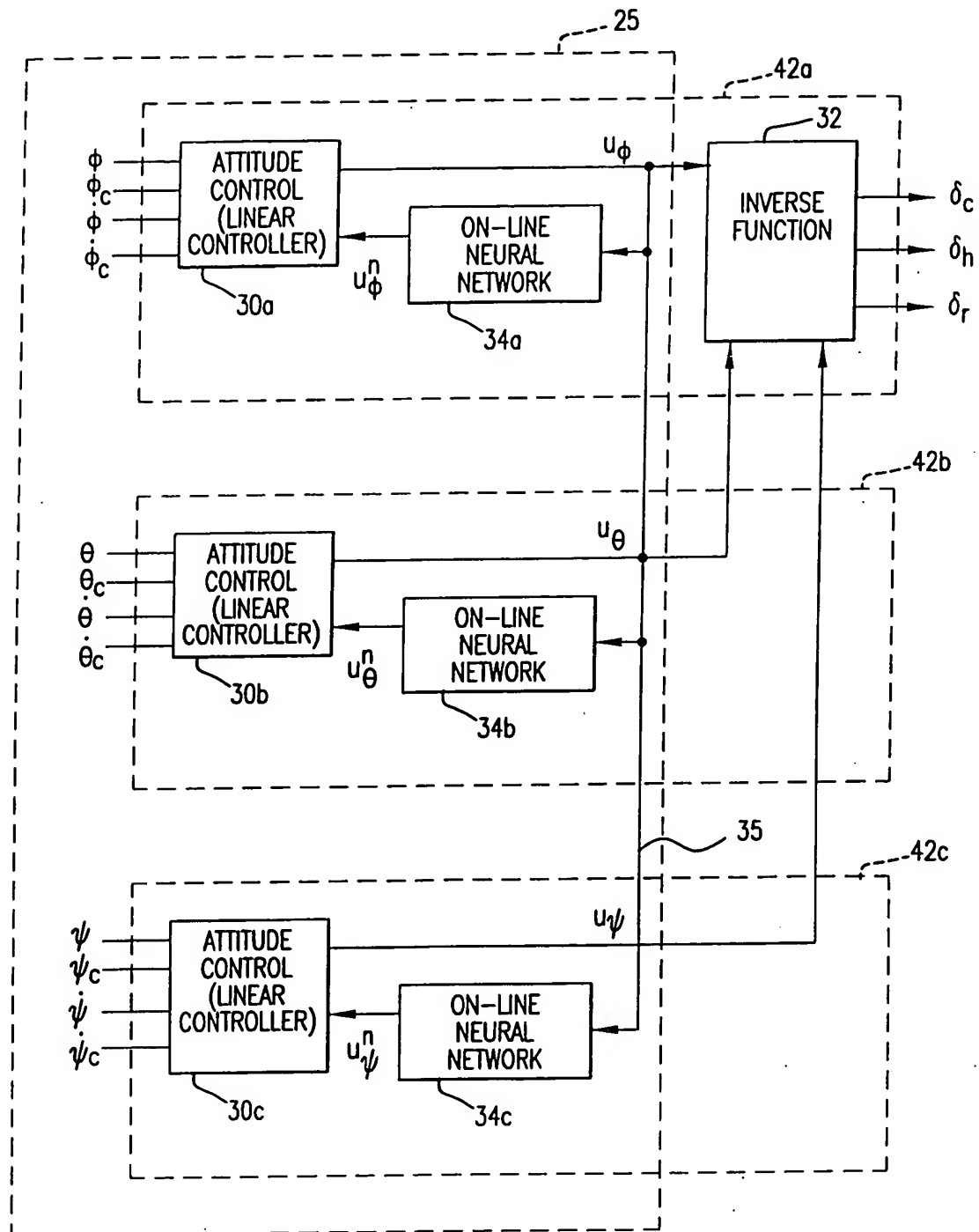


FIG.3

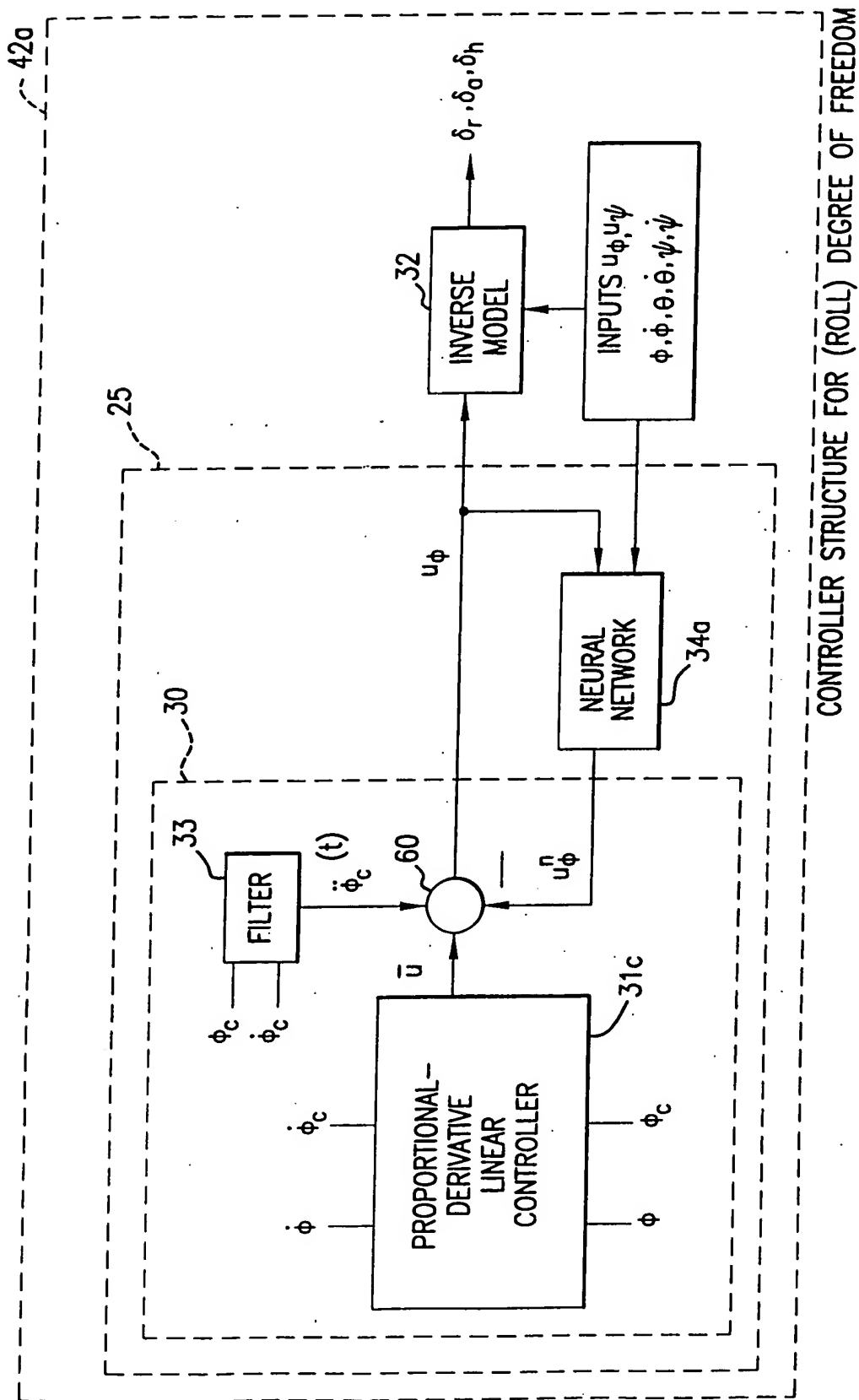
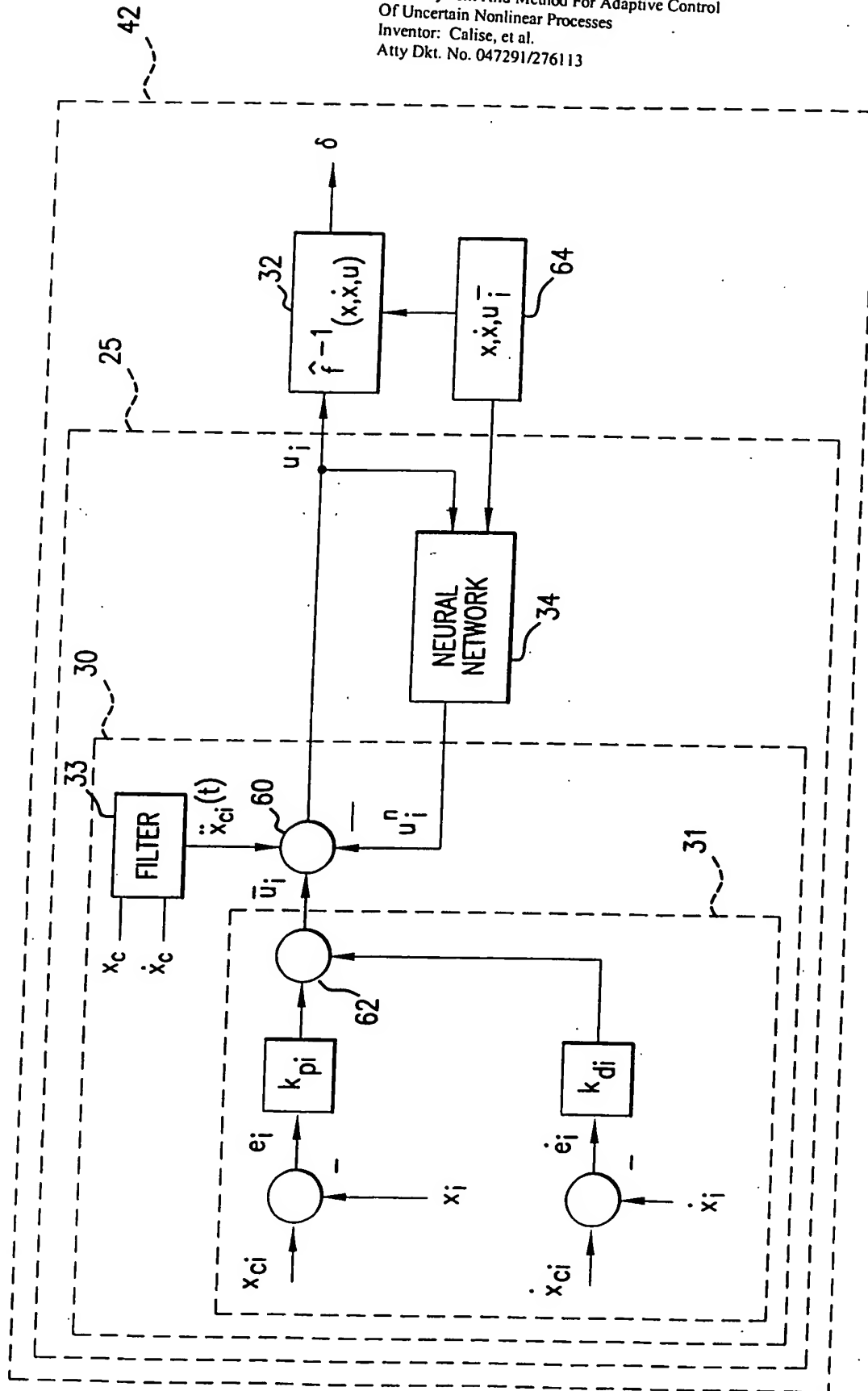


FIG. 4A



GENERAL CONTROLLER STRUCTURE FOR i th NODE

FIG.4B

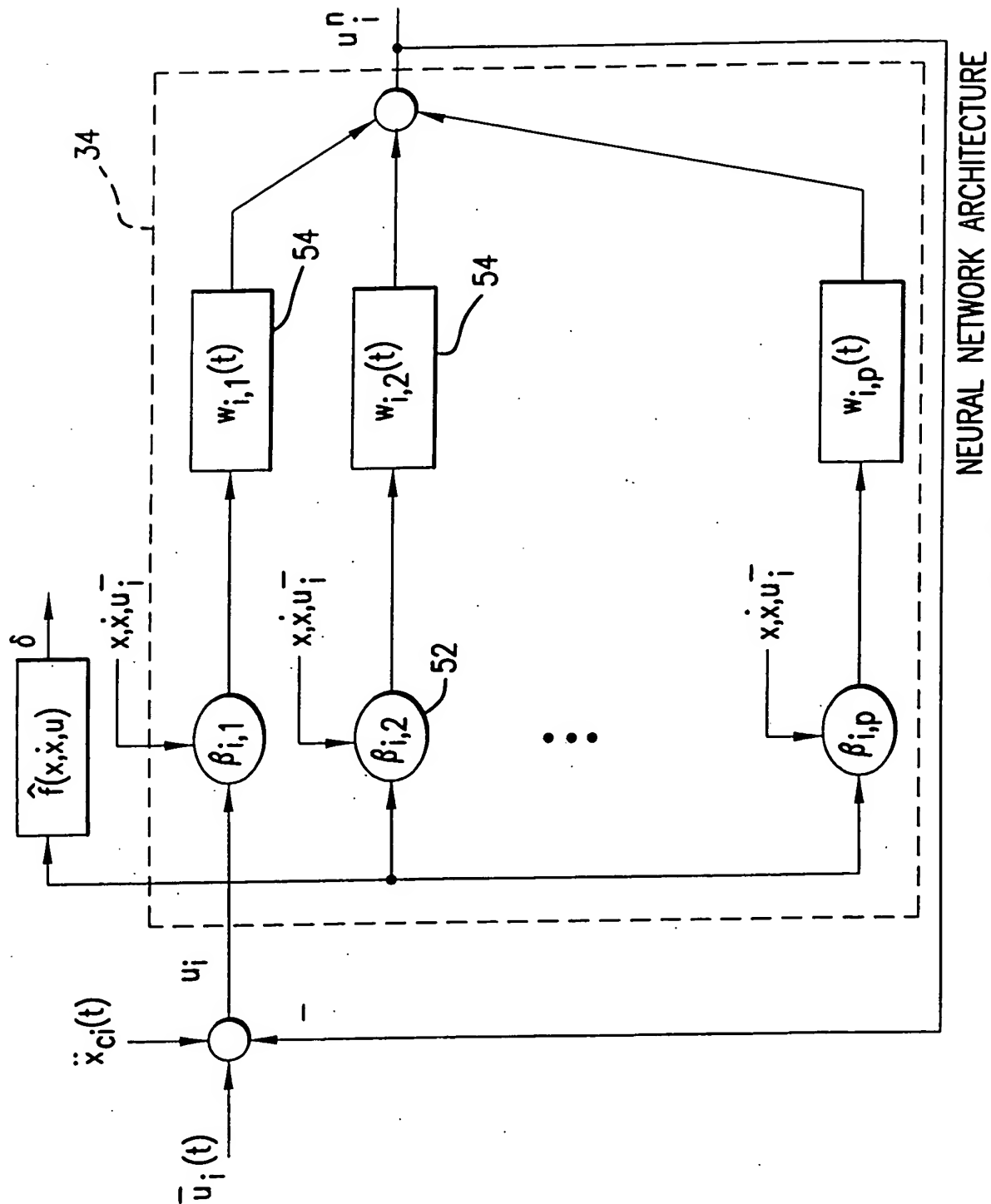


FIG. 5

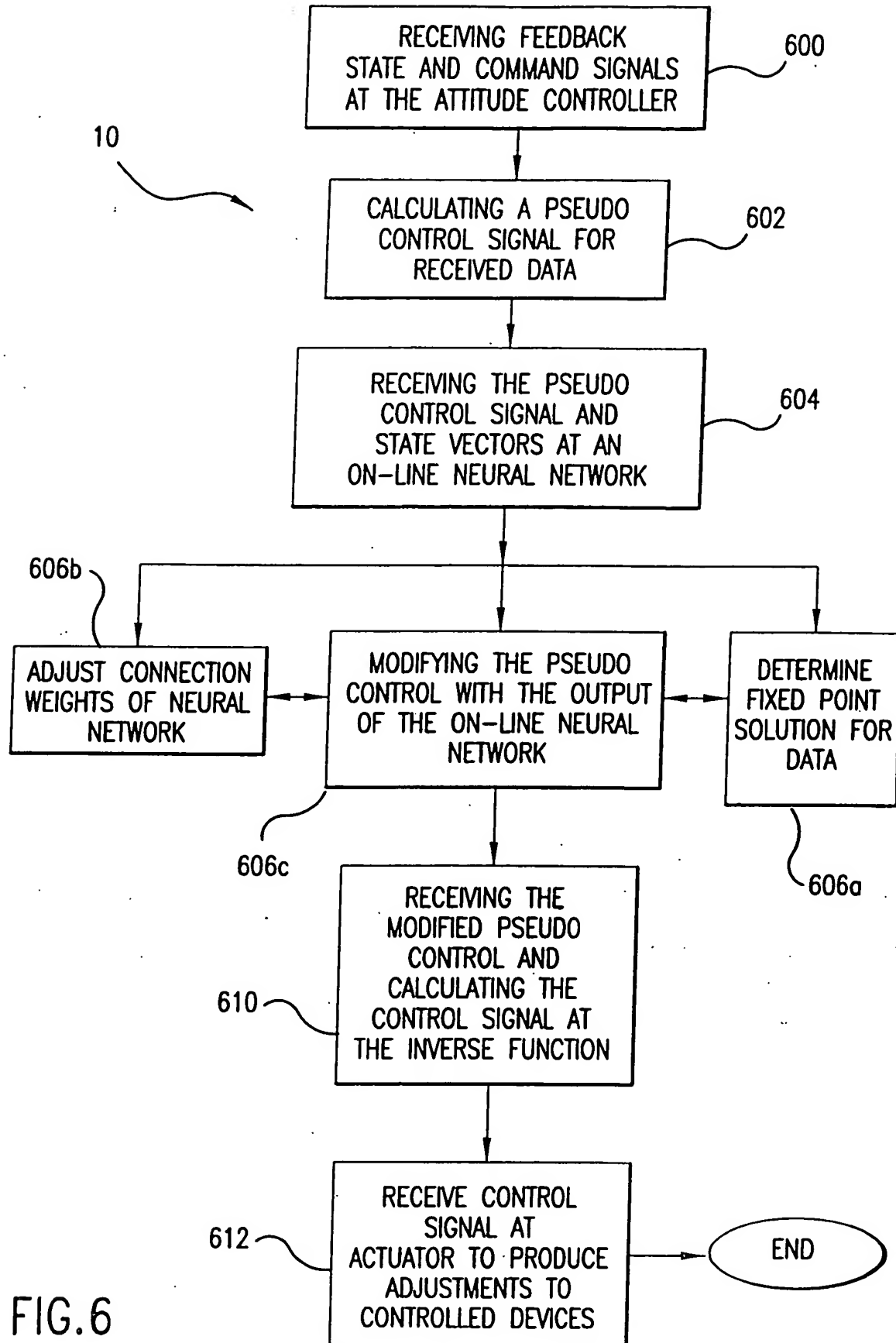


FIG.6

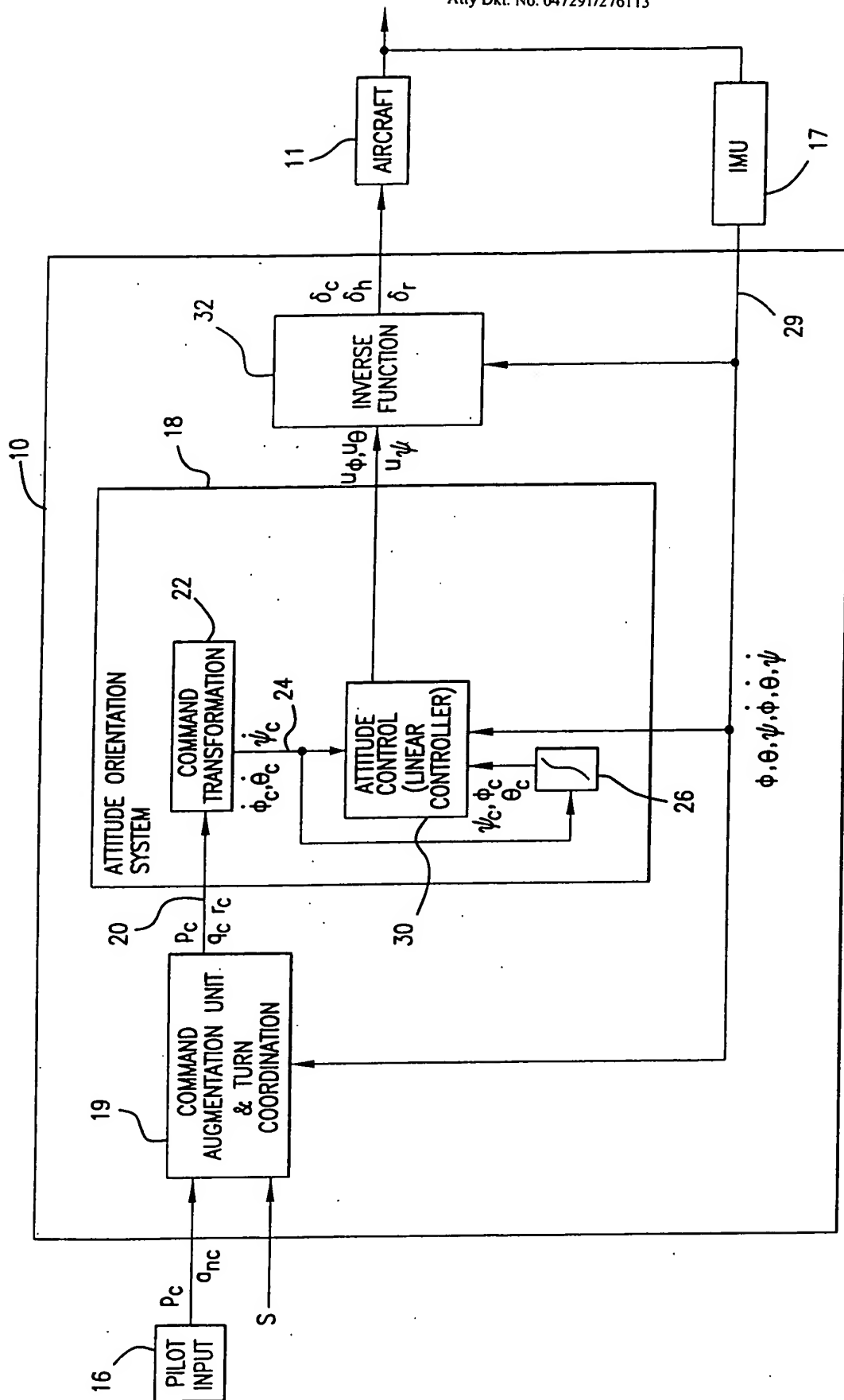


FIG.7

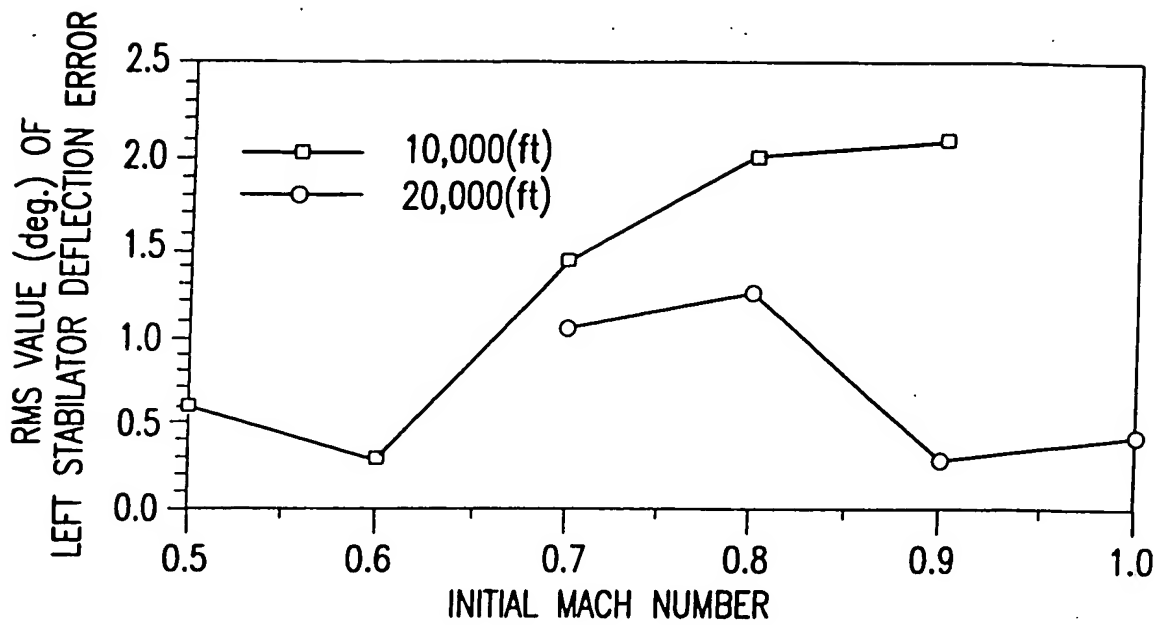


FIG.8A

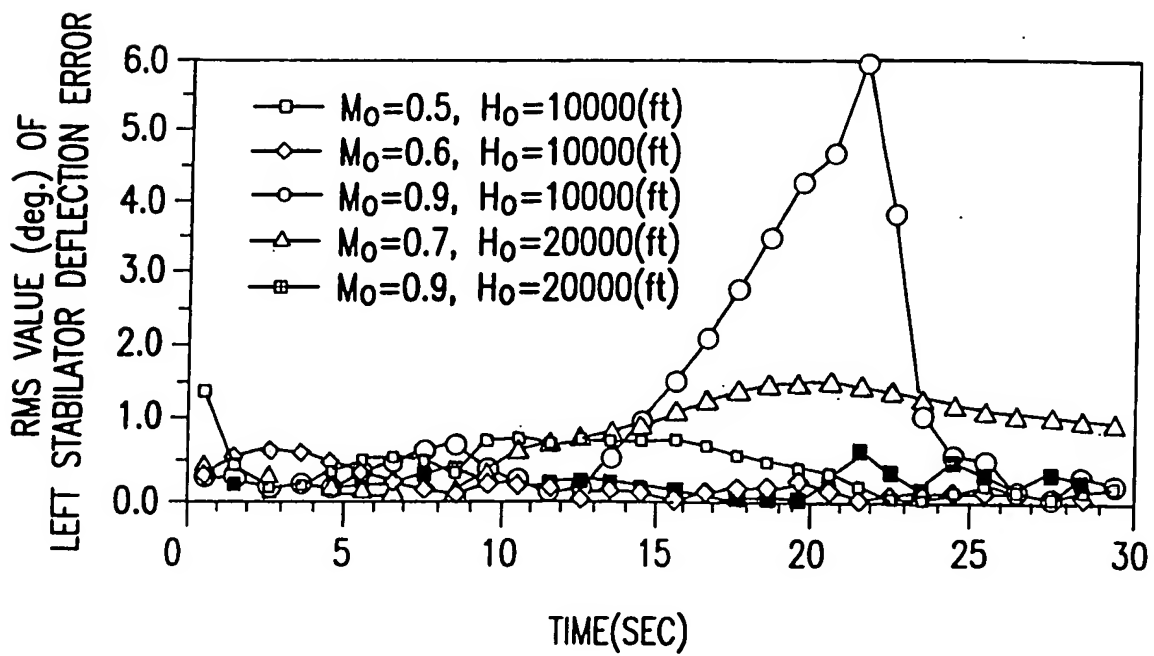


FIG.8B

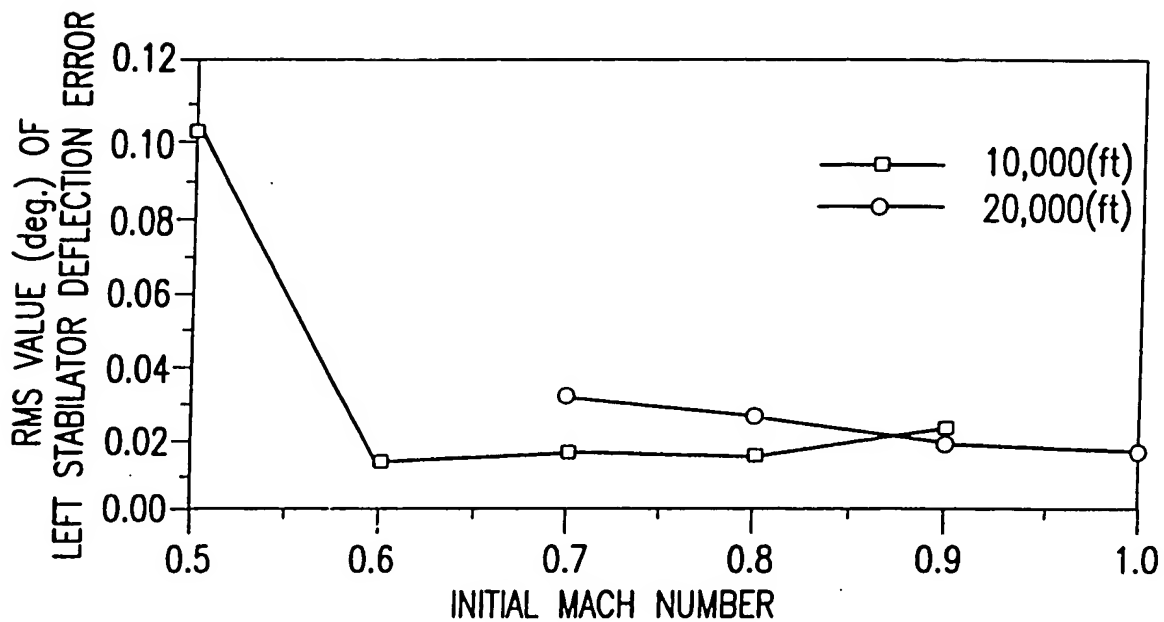


FIG.9A

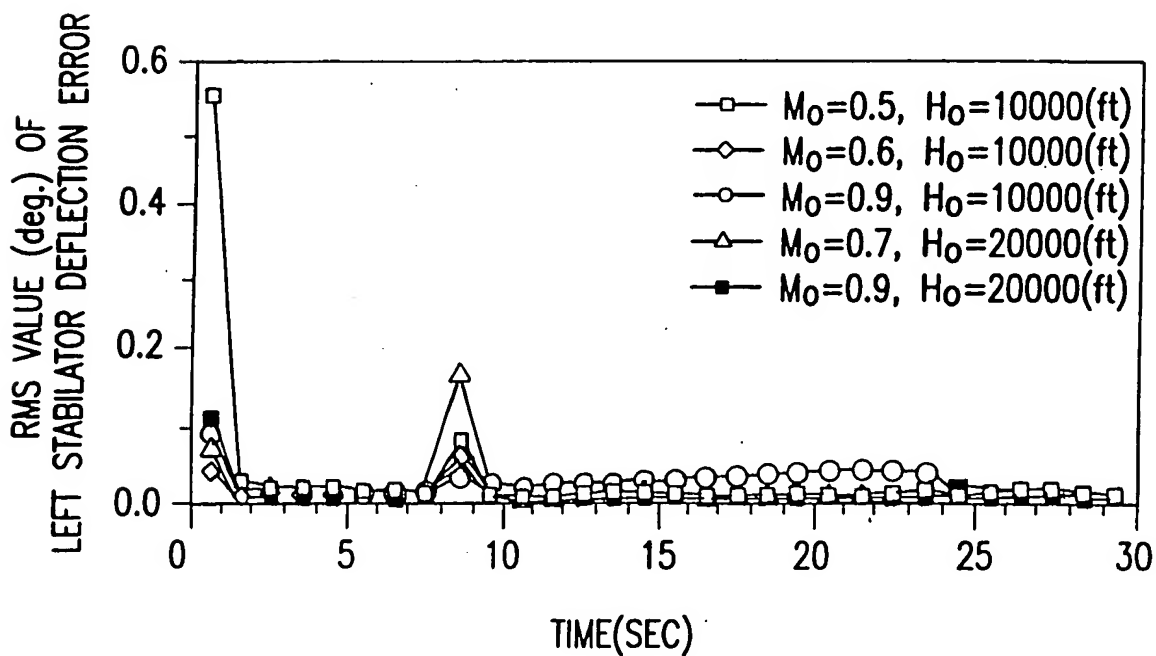


FIG.9B